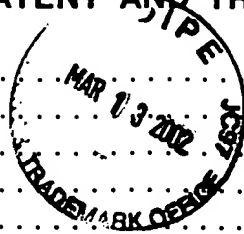


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 09/696,899
Priority Filing Date October 25, 2000
Inventor Howard E. Rhodes
Assignee Micron Technology, Inc.
Priority Group Art Unit 2818
Priority Examiner D. Le
Attorney's Docket No. MI22-1785
Title: Integrated Circuitry Fabrication Method of Making a Conductive
Electrical Connection, Method of Forming a Capacitor and an
Electrical Connection Thereto, Method of Forming DRAM Circuitry,
Integrated Circuitry, and DRAM Integrated Circuitry



EL844049517US

PRELIMINARY AMENDMENT

To: Box PATENT APPLICATION
Assistant for Patents and Trademarks
Washington, D.C. 20231

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)
Wells, St. John, Roberts, Gregory & Matkin P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99204-0317

RECEIVED
MAR 22 2002
TC 1700

AMENDMENTS

Please enter the following amendments prior to examining the above-identified application.

In the Title

Please replace the title with the following:

--Method of Forming a Capacitor and an Electrical Connection Thereto, and
Method of Forming DRAM Circuitry--.